

FEATURES

- InGaP HBT Technology
- -47 dBc ACPR @ ±10 MHz, +27 dBm
- 29 dB Gain
- High Efficiency
- Low Transistor Junction Temperature
- Matched for a 50 Ω System
- Low Profile Miniature Surface Mount Package; RoHS Compliant
- Multi-Carrier Capability

APPLICATIONS

- LTE, WCDMA, and HSDPA Air Interfaces
- Picocell, Femtocell, Home Nodes
- Customer Premises Equipment (CPE)



PRODUCT DESCRIPTION

The AWB7224 is a fully matched, Multi-Chip-Module (MCM) designed for picocell, femtocell, and customer premises equipment (CPE) applications. Consisting of two parallel path high linearity, high efficiency power amplifiers, the device meets the extremely demanding needs of small cell infrastructure architectures. Designed for LTE, WCDMA and HSDPA air interfaces operating in the 728 MHz to 768 MHz bands, the AWB7224 delivers up to +27 dBm of LTE (E-TM1.1) power through an external 90-degree hybrid coupler,

with an ACPR of -47 dBc. The device operates from a convenient +4.5 V supply and provides 29 dB of RF gain. The AWB7224 is manufactured using an advanced InGaP HBT MMIC technology offering state-of-the-art reliability, temperature stability, and ruggedness. Its 7 mm x 7 mm x 1.3 mm surface mount package incorporates RF matching networks optimized for output power, efficiency, and linearity in a 50 Ω system.

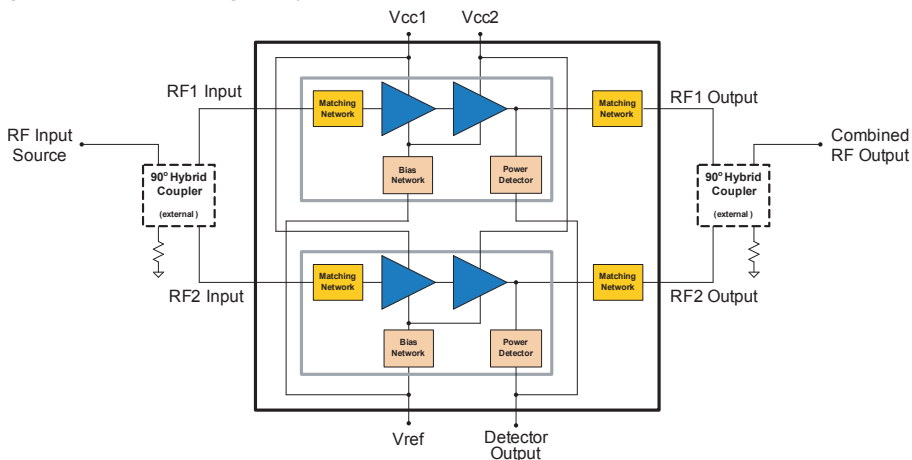


Figure 1: Block Diagram

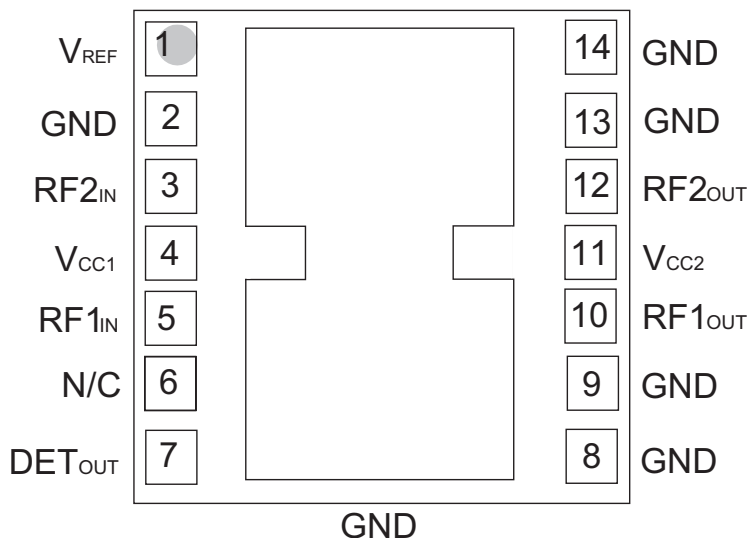


Figure 2: Pinout (X-ray Top View)

Table 1: Pin Description

| PIN | NAME | DESCRIPTION |
|-----|--------------------|-------------------|
| 1 | V _{REF} | Reference Voltage |
| 2 | GND | Ground |
| 3 | RF2 _{IN} | RF2 Input |
| 4 | V _{CC1} | Supply Voltage |
| 5 | RF1 _{IN} | RF1 Input |
| 6 | N/C | No Connection |
| 7 | DET _{OUT} | Detector Output |
| 8 | GND | Ground |
| 9 | GND | Ground |
| 10 | RF1 _{OUT} | RF1 Output |
| 11 | V _{CC2} | Supply Voltage |
| 12 | RF2 _{OUT} | RF2 Output |
| 13 | GND | Ground |
| 14 | GND | Ground |

ELECTRICAL CHARACTERISTICS

Table 2: Absolute Minimum and Maximum Ratings

| PARAMETER | MIN | MAX | UNIT |
|--|----------------------|--------|----------------|
| Supply Voltage (V _{CC}) | 0 | +5 | V |
| Reference Voltage (V _{REF}) | 0 | +3.5 | V |
| RF Output Power (P _{OUT}) ⁽¹⁾ | - | +30 | dBm, modulated |
| RF Input Power (P _{IN}) | - | +10 | dBm, CW |
| ESD Rating Human Body Model ⁽²⁾ Charged Device Model ⁽³⁾ | Class 1C Class IV | - - | |
| MSL Rating ⁽⁴⁾ | 4 | - | |
| Junction Temperature (T _J) | - | +150 | °C |
| Storage Temperature (T _{STG}) | -40 | +150 | °C |

Functional operation is not implied under these conditions. Exceeding any one or a combination of the Absolute Maximum Rating Conditions may cause permanent damage to the device. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

Notes:

- (1) At output of external 90° hybrid coupler.
- (2) JEDEC JS-001-2010.
- (3) JEDEC JESD22-C101D.
- (4) 260 °C peak reflow.

Table 3: Operating Ranges

| PARAMETER | MIN | TYP | MAX | UNIT | COMMENTS |
|--|------------|------------|---------------|------|---------------------------|
| Operating Frequency (f) | 728 | - | 768 | MHz | |
| Supply Voltage (V _{CC}) | +3.6 | +4.5 | +4.65 | V | |
| Reference Voltage (V _{REF}) | +2.80 0 | +2.85 - | +2.90 +0.5 | V | PA "on" PA "shut down" |
| RF Output Power (P _{OUT}) ⁽¹⁾ | - | +27 | - | dBm | |
| Case Temperature (T _C) ⁽²⁾ | -40 | - | +85 | °C | |

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

Notes:

- (1) Typ RF Output Power is used during production test.
- (2) Case Temperature references the board temperature at the ground paddle on the backside of the package.

Table 4: Electrical Specifications
(T_C = +25 °C, V_{CC} = +4.5 V, V_{REF} = +2.85 V, 50 Ω system)

| PARAMETER | MIN | TYP | MAX | UNIT | COMMENTS |
|--|--------|------------|------------|------|--|
| Gain ⁽²⁾ | 27 | 29 | 34 | dB | 728 - 768 MHz |
| ACPR ^{(1), (2), (3)} @ 10 MHz @ 20 MHz | - - | -47 -57 | -45 -54 | dBc | |
| Power-Added Efficiency ^{(1), (2), (3)} | 11.5 | 13 | - | % | |
| Thermal Resistance (R _{JC}) ⁽⁴⁾ | - | 17 | - | °C/W | Junction to Case |
| Supply Current ^{(1), (2), (3)} | - | 825 | 968 | mA | total through V _{CC} pins |
| Quiescent Current (I _q) | 200 | 275 | 340 | mA | |
| Reference Current | 7 | 10 | 13 | mA | through V _{REF} pin |
| Leakage Current | - | 3 | 10 | μA | V _{CC} = +5 V, V _{REF} = 0 V |
| Harmonics ⁽²⁾ 2f _o 3f _o , 4f _o | - - | -53 -65 | -43 -53 | dBc | |
| Input Return Loss ⁽²⁾ | 15 | 20 | - | dB | |
| Output Return Loss ⁽²⁾ | 15 | 20 | - | dB | |
| P1dB | - | +35.5 | - | dBm | CW tone |
| Spurious Output Level ⁽²⁾ (all spurious outputs) | - | - | -60 | dBc | P _{OUT} ≤ +27 dBm In-band load VSWR < 5:1 Out-of-band load VSWR < 10:1 Applies over all voltage and temperature operating ranges |
| Load mismatch stress with no permanent degradation or failure ⁽²⁾ | 8:1 | - | - | VSWR | V _{CC} = +4.5 V, P _{OUT} = +27 dBm Applies over full operating temperature range |

Notes:

- (1) ACPR and Efficiency measured at 748 MHz.
(2) P_{OUT} = +27 dBm, using specified external 90° hybrid couplers.
(3) LTE E-TM1.1 (10 MHz).

APPLICATION INFORMATION

To ensure proper performance, refer to all related Application Notes.

Shutdown Mode

The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to the VREF voltage.

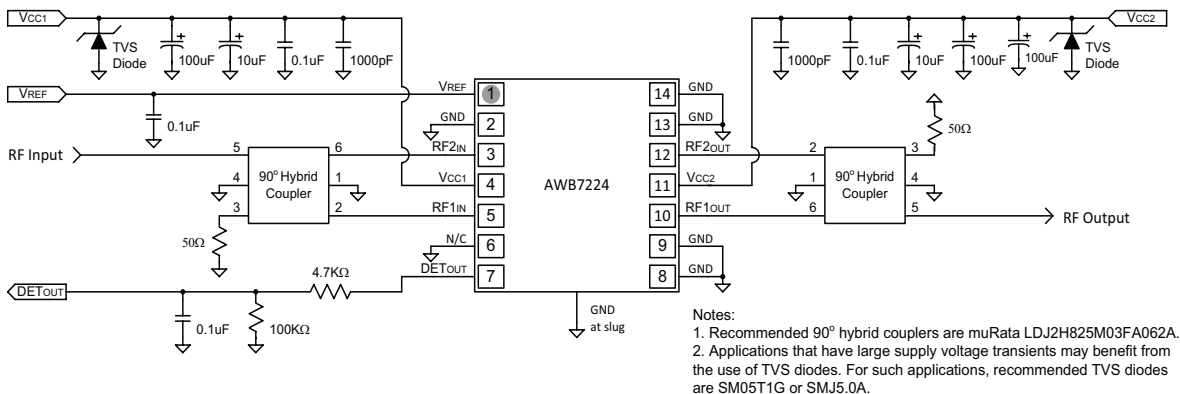
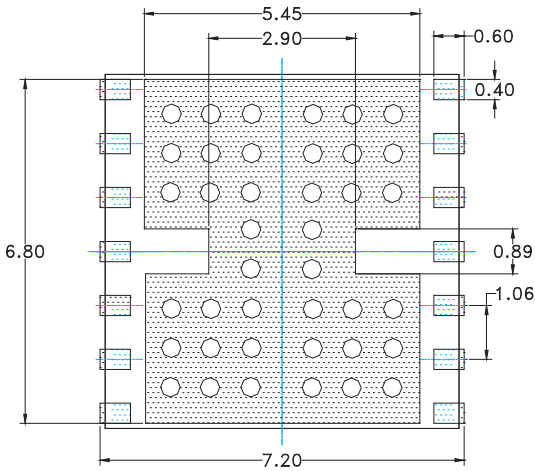


Figure 3: Application Circuit Schematic

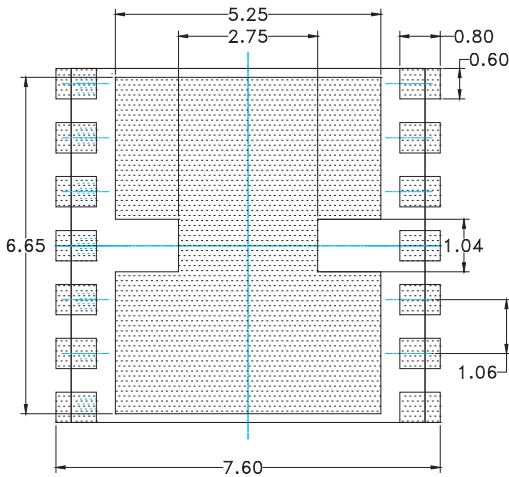


PCB METAL
TOP (X-RAY) VIEW

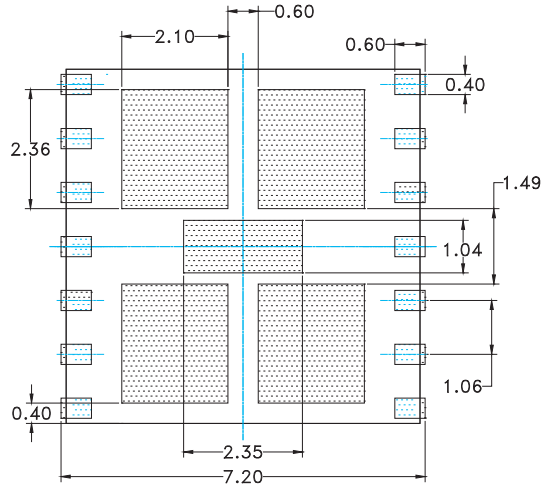
ONLY PACKAGE I/O's AND
GROUND REQUIREMENTS
SHOWN.

NOTES:

- (1) UNLESS SPECIFIED DIMENSIONS ARE SYMMETRICAL ABOUT CENTER LINES SHOWN.
- (2) DIMENSIONS IN MILLIMETERS.
- (3) VIAS SHOWN IN PCB METAL VIEW ARE FOR REFERENCE ONLY. NUMBER & SIZE OF THERMAL VIAS REQUIRED DEPENDENT ON HEAT DISSIPATION REQUIREMENT AND THE PCB PROCESS CAPABILITY.



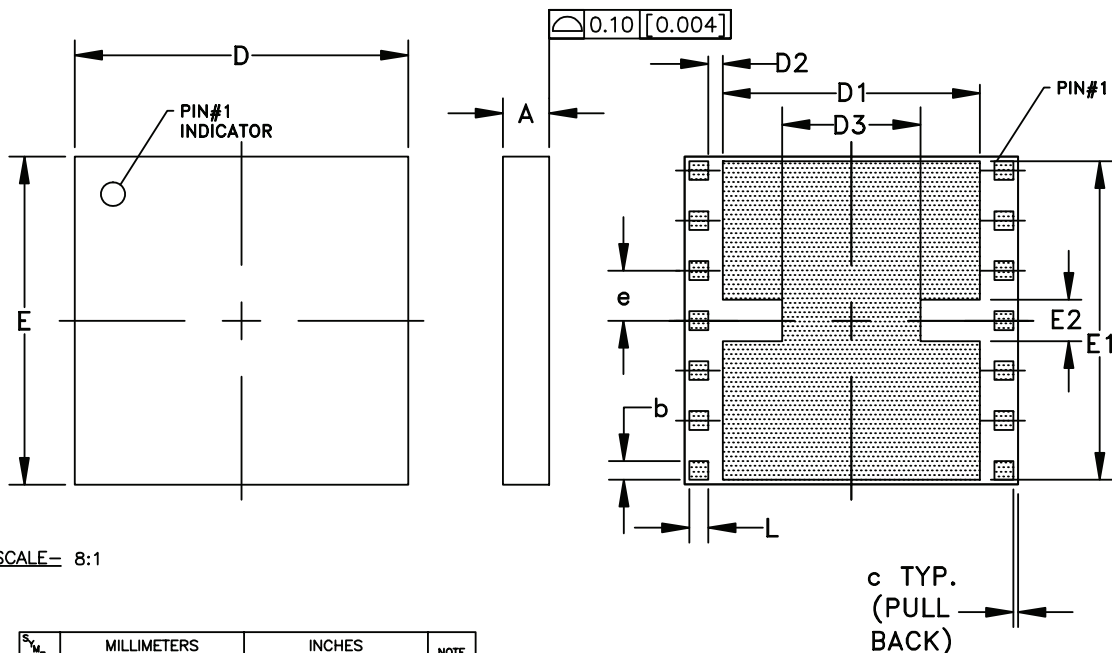
PCB SOLDER MASK
TOP (X-RAY) VIEW



STENCIL APERTURE
TOP (X-RAY) VIEW

Figure 4: PCB Footprint

PACKAGE OUTLINE



SCALE= 8:1

| Symbol | MILLIMETERS | | | INCHES | | | NOTE |
|--------|-------------|-------|-------|--------|--------|--------|------|
| | MIN. | NOM. | MAX. | MIN. | NOM. | MAX. | |
| A | 1.17 | 1.27 | 1.37 | 0.046 | 0.05 | 0.054 | — |
| b | 0.375 | 0.400 | 0.425 | 0.0148 | 0.0157 | 0.0167 | 14X |
| c | — | 0.10 | — | — | 0.004 | — | — |
| D | 6.90 | 7.00 | 7.10 | 0.272 | 0.276 | 0.280 | — |
| D1 | — | 5.40 | — | — | 0.213 | — | — |
| D2 | — | 0.30 | — | — | 0.0118 | — | — |
| D3 | — | 2.90 | — | — | 0.114 | — | — |
| E | 6.90 | 7.00 | 7.10 | 0.272 | 0.276 | 0.280 | — |
| E1 | — | 6.80 | — | — | 0.268 | — | — |
| E2 | — | 0.89 | — | — | 0.035 | — | — |
| e | — | 1.067 | — | — | 0.0420 | — | 6X |
| L | 0.375 | 0.400 | 0.425 | 0.0148 | 0.0157 | 0.0167 | 14X |

NOTES:

1. CONTROLLING DIMENSIONS: MILLIMETERS
2. UNLESS SPECIFIED TOLERANCE=±0.076[0.003].
3. PADS (INCLUDING CENTER) SHOWN UNIFORM SIZE FOR REFERENCE ONLY. ACTUAL PAD SIZE AND LOCATION WILL VARY WITHIN MIN. AND MAX. DIMENSIONS ACCORDING TO SPECIFIC LAMINATE DESIGN.

Figure 5: Package Outline - 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module

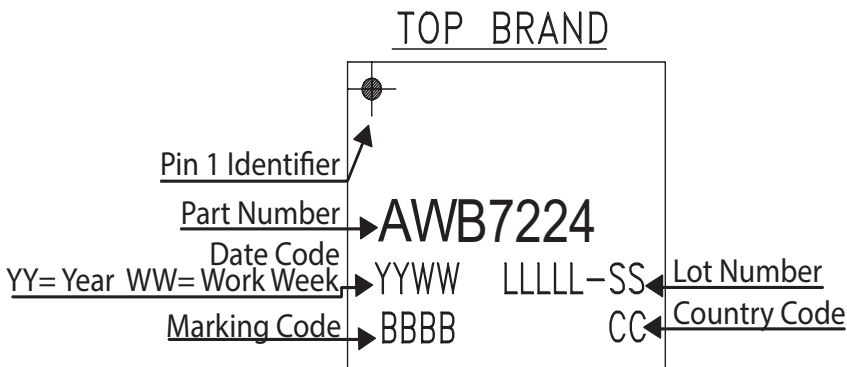


Figure 6: Branding Specification

COMPONENT PACKAGING

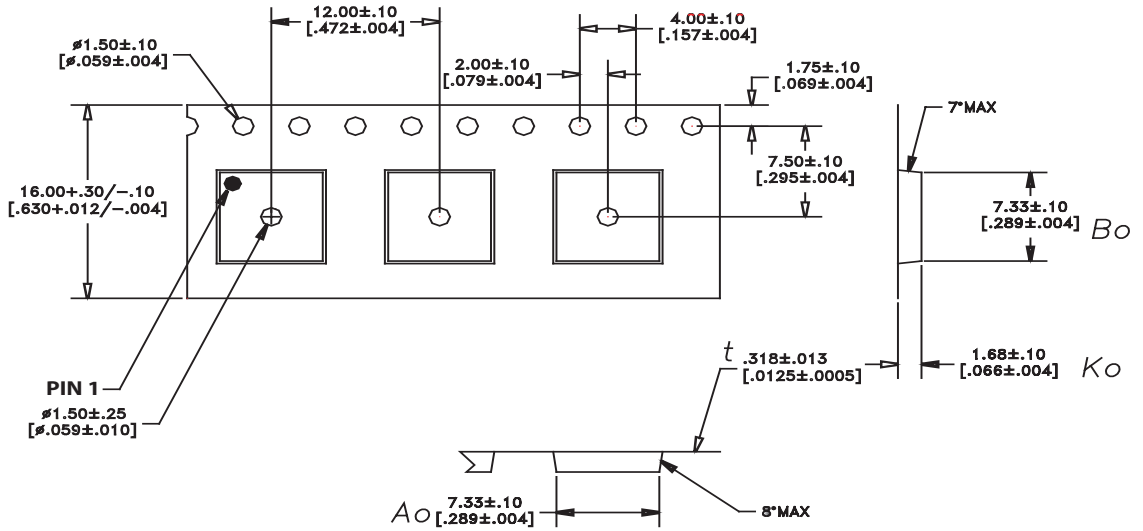


Figure 7: Tape & Reel Packaging

Table 5: Tape & Reel Dimensions

| PACKAGE TYPE | TAPE WIDTH | POCKET PITCH | REEL CAPACITY | MAX REEL DIA |
|----------------------|------------|--------------|---------------|--------------|
| 7 mm x 7 mm x 1.3 mm | 16 mm | 12 mm | 2500 | 13" |

ORDERING INFORMATION

| ORDER NUMBER | TEMPERATURE RANGE | PACKAGE DESCRIPTION | COMPONENT PACKAGING |
|--------------|-------------------|---|-------------------------------------|
| AWB7224P7 | -40 °C to +85 °C | RoHS-compliant 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module | Loose in Bag |
| AWB7224P8 | -40 °C to +85 °C | RoHS-compliant 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module | Tape and Reel, 2500 pieces per Reel |
| AWB7224P9 | -40 °C to +85 °C | RoHS-compliant 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module | Partial Reel |

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